

# GaAs INTEGRATED CIRCUIT $\mu PG153TB$

## L-BAND SPDT SWITCH

#### **DESCRIPTION**

The  $\mu$ PG153TB is an L-band SPDT (Single Pole Double Throw) GaAs FET switch which was developed for digital cellular or cordless telephone application. The device can operate from 100 MHz to 2.5 GHz, having the low insertion loss. It housed in an original 6-pin super minimold package that is smaller than usual 6-pin minimold easy to install and contributes to miniaturizing the system.

#### **FEATURES**

Low Insertion Loss
 Lins = 0.7 dB TYP. @Vcont = +3.0 V/0 V, f = 2 GHz

High Linearity Switching: Pin (1 dB) = +33.0 dBm TYP. @Vcont = +3.0 V/0 V, f = 2 GHz

Small 6-pin super minimold package (Size: 2.0 × 1.25 × 0.9 mm)

#### **APPLICATIONS**

- · L, S-band digital cellular or cordless telephone
- · PCS, WLAN, and WLL applications

## ORDERING INFORMATION

Part Number	Marking	Package	Supplying Form
μPG153TB-E3	G1J	6-pin super minimold	Embossed tape 8 mm wide. Pin 1, 2, 3 face to tape perforation side. Qty 3 kp/reel.

**Remark** To order evaluation samples, please contact your local NEC sales office. (Part number for sample order:  $\mu$ PG153TB)

# ABSOLUTE MAXIMUM RATINGS $(T_A = +25^{\circ}C)$

Parameter	Symbol	Ratings	Unit
Control Voltage 1, 2	VCONT1, 2	-6.0 to +6.0 <sup>Note</sup>	V
Input Power	Pin	+33	dBm
Total Power Dissipation	Ptot	0.15	W
Operating Temperature	TA	-45 to +85	°C
Storage Temperature	T <sub>stg</sub>	-55 to +150	°C

**Note** Condition  $2.5 \le |V_{CONT1} - V_{CONT2}| \le 6.0 \text{ V}$ 

**Remarks 1.** Mounted on a  $50 \times 50 \times 1.6$  mm double copper clad epoxy glass PWB, T<sub>A</sub> = +85°C

2. Operation in excess of any one of these parameters may result in permanent damage.

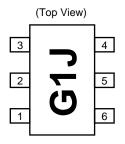
Caution The IC must be handled with care to prevent static discharge because its circuit is composed of GaAs MES FET.

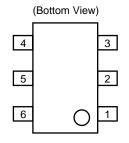
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## **PIN CONNECTIONS**

Pin No.	Connection	Pin No.	Connection
1	OUT1	4	V <sub>CONT2</sub>
2	GND	5	IN
3	OUT2	6	V <sub>CONT1</sub>





## RECOMMENDED OPERATING CONDITIONS ( $T_A = +25$ °C)

Parameter	Symbol	MIN.	TYP.	MAX.	Unit
Control Voltage (Low)	VCONT	-0.2	0	+0.2	V
Control Voltage (High)	VCONT	+2.5	+3.0	+5.3	V

#### **ELECTRICAL CHARACTERISTICS**

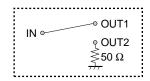
(Unless otherwise specified,  $T_A = +25$ °C,  $V_{CONT1} = 3$  V,  $V_{CONT2} = 0$  V or  $V_{CONT1} = 0$  V,  $V_{CONT2} = 3$  V,  $V_{$ 

Parameter	Symbol	Test Conditions	MIN.	TYP.	MAX.	Unit
Insertion Loss	Lins	f = 100 M to 1.0 GHz	-	0.50	0.65	dB
		f = 2.0 GHz	_	0.70	0.90	
		f = 2.5 GHz	_	0.90 <sup>Note 1</sup>	-	
Isolation	ISL	f = 100 M to 2.0 GHz	10	13	-	dB
		f = 1.0 GHz	15	18.5	-	
		f = 2.5 GHz	_	10 <sup>Note 1</sup>	-	
Input Return Loss	RLin	f = 100 M to 2.0 GHz	11	15	-	dB
Output Return Loss	RLout	f = 100 M to 2.0 GHz	11	15	-	dB
Input Power at 0.1 dB Compression Point <sup>Note 2</sup>	Pin (0.1 dB)	f = 2.0 GHz	-	29	-	dBm
Input Power at 1 dB Compression Point <sup>Note 2</sup>	Pin (1 dB)	f = 2.0 GHz	31	33	-	dBm
Switching Speed	tsw		_	30	-	ns
Control Current	Ісонт	Vcont = 3 V/0 V	-	20	50	μΑ

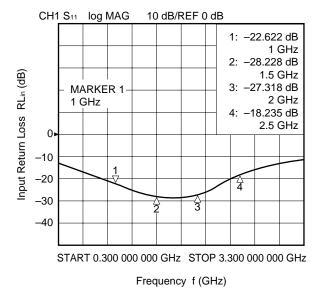
- **Notes 1.** Characteristic for reference at f = 2.0 to 2.5 GHz
  - 2. Pin (0.1 dB) or Pin (1 dB) is measured the input power level when the insertion loss increases more 0.1 dB or 1 dB than that of linear range. All other characteristics are measured in linear range.
- Cautions 1. When the  $\mu$ PG153TB is used, it is necessary to use DC blocking capacitors for No. 1 (OUT1), No. 3 (OUT2) and No. 5 (IN). The value of DC blocking capacitors should be chosen to accommodate the frequency of operation, band width, switching speed and the condition with actual board of your system.
  - The range of recommended DC blocking capacitor value is less than 100 pF.
  - 2. The distance between IC's GND pin and ground pattern of substrate should be as shorter as possible to avoid parasitic parameters.

## **★** TYPICAL CHARACTERISTICS (ON)

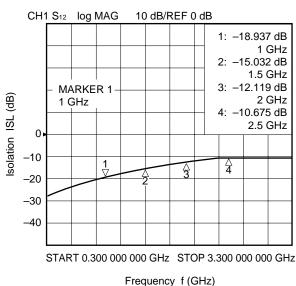
TEST CONDITIONS: VCONT = 3 V/0 V, Pin = 0 dBm, TA = +25 °C



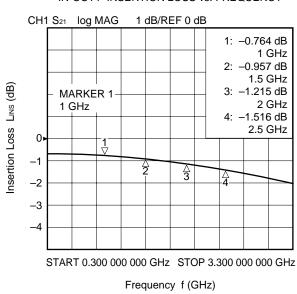
IN-OUT1 INPUT RETURN LOSS vs. FREQUENCY



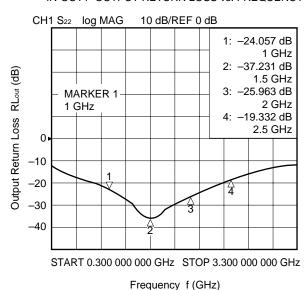
IN-OUT1 ISOLATION vs. FREQUENCY



IN-OUT1 INSERTION LOSS vs. FREQUENCY



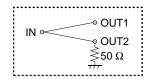
IN-OUT1 OUTPUT RETURN LOSS vs. FREQUENCY



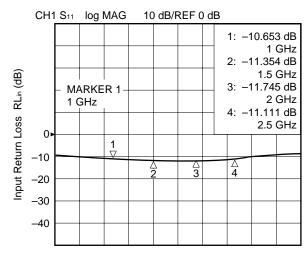
Caution This data is including loss of the test fixture.

## **★** TYPICAL CHARACTERISTICS (OFF)

TEST CONDITIONS: VCONT = 3 V/3 V, Pin = 0 dBm, TA = +25 °C



#### IN-OUT1 INPUT RETURN LOSS vs. FREQUENCY

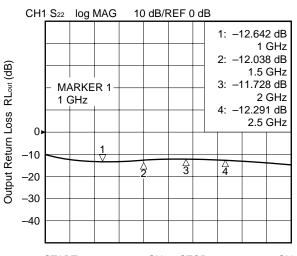


START 0.300 000 000 GHz STOP 3.300 000 000 GHz Frequency f (GHz)

## IN-OUT1 INSERTION LOSS vs. FREQUENCY

## CH1 S<sub>21</sub> log MAG 10 dB/REF 0 dB 1: -4.066 dB 1 GHz 2: -4.072 dB 1.5 GHz Insertion Loss Lins (dB) 3: -4.130 dB MARKER 1 2 GHz 1 GHz 4: -4.382 dB 2.5 GHz \_<u>∆</u> -3--10 -20-30 -40 START 0.300 000 000 GHz STOP 3.300 000 000 GHz Frequency f (GHz)

## IN-OUT1 OUTPUT RETURN LOSS vs. FREQUENCY

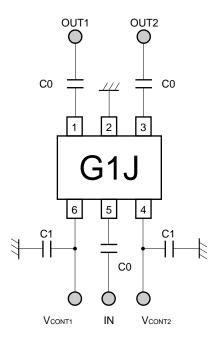


Caution This data is including loss of the test fixture.

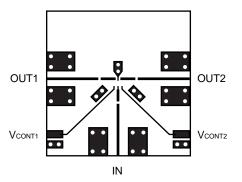


## **TEST CIRCUIT**

 $T_A = +25$ °C,  $V_{CONT1} = +3$  V,  $V_{CONT2} = 0$  V or  $V_{CONT1} = 0$  V,  $V_{CONT2} = +3$  V, f = 2 GHz,  $Z_0 = 50$   $\Omega$  Off chip DC blocking capacitors value:  $C_0 = 51$  pF,  $C_1 = 1$  000 pF (Bypass), using NEC standard evaluation board



## **★** EVALUATION BOARD



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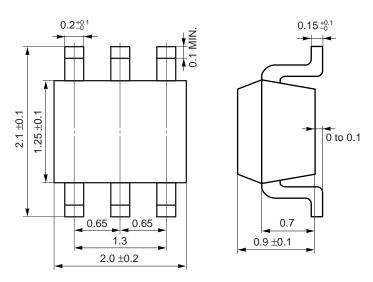
## TRUTH TABLE OF SWITCHING BY CONDITION OF CONTROL VOLTAGE

		Vcont1				
		Vcont1(H)		Vcont(l)		
Vcont2	Vcont(H)	IN —O	Note O— OUT1 O— OUT2	IN —O	O— OUT1 O— OUT2	
	Vcont(L)	IN —	O— OUT1	ix —O	Note O—OUT1 O—OUT2	

**Note** In case of VCONT1 = VCONT2 = High or VCONT1 = VCONT2 = Low, (that is same control voltage for both pins), input signal of IN (Pin 5) is output from OUT1 (Pin 1) and OUT2 (Pin 3).

## PACKAGE DIMENSIONS

## 6 PIN MINIMOLD (Unit: mm)





## RECOMMENDED SOLDERING CONDITIONS

This product should be soldered under the following recommended conditions. For soldering method and conditions other than those recommended below, contact your NEC sales representative.

Soldering Method	Soldering Conditions	Recommended Condition Symbol
Infrared Reflow	Package peak temperature: 235°C or below Time: 30 seconds or less (at 210°C) Count: 3, Exposure limit: None <sup>Note</sup>	IR35-00-3
VPS	Package peak temperature: 215°C or below Time: 40 seconds or less (at 200°C) Count: 3, Exposure limit: None <sup>Note</sup>	VP15-00-3
Wave Soldering	Soldering bath temperature: 260°C or below Time: 10 seconds or less Count: 1, Exposure limit: None <sup>Note</sup>	WS60-00-1
Partial Heating	Pin temperature: 300°C Time: 3 seconds or less (per pin row) Exposure limit: None <sup>Note</sup>	_

Note After opening the dry pack, keep it in a place below 25°C and 65% RH for the allowable storage period.

Data Sheet P13632EJ2V0DS00

Caution Do not use different soldering methods together (except for partial heating).

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## Caution

The Great Care must be taken in dealing with the devices in this guide.

The reason is that the material of the devices is GaAs (Gallium Arsenide), which is designated as harmful substance according to the law concerned.

Keep the law concerned and so on, especially in case of removal.

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